US Micron engineering candidate guide

Micron is looking for the brightest minds to join our international team of innovators.
Match your education and experience to see what jobs might be a fit for you.

Department	Job title(s)	Degree	Discipline (Major)	Preferred coursework		Job location(s)
Process Development/ Equipment R&D	• Process Development Engineer • Process Pathfinding Engineer • Equipment R&D Engineer • **Areas:** Photolithography, Dry Etch, Wet Process, CMP, Thin Films, Implant, Metrology, and RDA	• MS • PhD	• Chemistry/Chemical Engineering • Electrical Engineering • Microelectronics Engineering • Materials Science • Physics • Mechanical Engineering • Mechatronics	• Device or Processing Research • Semiconductor Processing Lab • Data Science Application in Engineering • Statistics • Design of Experiments (DOE)	• Computational Fluid Dynamics • Computational Materials • Thermo-Mechanical Modeling • AI-Assisted Modeling • Optics	• Boise, ID
Process Integration R&D	• Process Integration Engineer • Device Engineer • CMOS Engineer	• MS • PhD	• Chemistry/Chemical Engineering • Electrical Engineering • Microelectronics Engineering • Materials Science • Physics	• Semiconductor Physics • Device or Processing Research • CMOS • Design of Experiments (DOE)	• SPC, JMP • Circuit Design • MATLAB, SQL, Python, Java, or other Data Science coursework	• Boise, ID
Mask Technology Center	• Process Development Engineer • Reticle Manufacturing Engineer • R&D Process Engineer • Process Integration Engineer • Yield Engineer	• BS • MS • PhD	• Chemistry/Chemical Engineering • Mechanical Engineering • Materials Science	• Semiconductor Physics • Physics • Mathematics/Statistics • Optics	• SPC, Design of Experiments (DOE), JMP • C++, SQL, Python, Java	• Boise, ID
Mask Technology Group	• Scribe Design Engineer • CAD Engineer • Verification Engineer • RET/OPC Design Engineer • Tapeout Operations Engineer • Systems Automation Engineer	• BS • MS • PhD	• Electrical Engineering • Chemical Engineering • Physics • Optics Engineering • Computer Science	• Semiconductor Process and Microfabrication • Device Physics • Optics/Fourier Optics • Computational Optics and Simulations	• Python, Java • Database (SQL, etc.)	• Boise, ID
Global Quality	• Manufacturing Quality Engineer • Quality Data Analyst Engineer • Quality Product Reliability Engineer	• BS • MS • PhD	• Computer Engineering • Electrical Engineering • Mechanical Engineering	• CMOS Logic • Manufacturing • Project Management • Semiconductor Physics	• Statistics • C++ • VLSI Design	• Boise, ID • Longmont, CO
Test and Probe	• Test Engineer • Equipment Engineer • Process Engineer • Manufacturing Engineer • Software Engineer	• BS • MS	• Computer Engineering • Electrical Engineering • Industrial Engineering • Mechanical Engineering • Computer Science	• Digital Logic • Electrical/Electronics/ Mechatronics • Operations/Project Management • Systems Engineering	• Programming: Python, C++, Perl • Statistics • Design of Experiments (DOE) • UNIX • CAD	• Boise, ID • Clay, NY (coming soon)
Firmware Development	• Firmware Architect • Firmware Developer • Firmware Test Architect • Firmware Tester	• BS • MS • PhD	• Computer Engineering • Electrical Engineering	• Programming Projects in C/ C++ • Digital Design • Digital Devices • Control Theory	• Computer Architectures • Digital Signal Processing • Real-Time Systems	• Boise, ID • Folsom, CA, • San Jose, CA, • Longmont, CO

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Advanced Packaging Technology Development	• Package Development Engineer • Semiconductor Packaging Engineer • Process Development Engineer • Process Integration Engineer • Package Modeling • Substrate Designer	• MS • PhD	• Chemistry/Chemical Engineering • Electrical Engineering • Microelectronics Engineering • Materials Science • Mechanical Engineering • Mechatronics/Physics • Metallurgical Engineer • Polymer Science	• Semiconductor Packaging • Device or Processing Research • Semiconductor Processing Lab • Data Science Application in Engineering • Thermal Modeling • Mechanical Modeling	• Statistics and Design of Experiments (DOE) • Computational Fluid Dynamics • Computational Materials • Thermo–Mechanical Modeling • AI–Assisted Modeling • Python Coding	• Boise, ID
HBM/DRAM Engineering	• Design Engineer • Product Engineer • Verification/Validation Engineer • Layout Engineer	• BS • MS • PhD	• Computer Engineering • Electrical Engineering	At least one from: • CMOS Logic • Digital Logic • VLSI Design • Analog Circuit Design • RTL Circuit Design	• C++ • C# • Semiconductor Physics	• Atlanta, GA • Boise, ID • Folsom, CA • Richardson, TX
Central Engineering	• CAD Engineer • GEL System Compatibility/Lab Engineer • Memory System Architect/Engineer • Data Scientist/AI–ML/ GenAI Architect/Engineer • HBM Support	• BS • MS • PhD	• Computer Engineering • Electrical Engineering • Chemistry/Chemical Engineering • Microelectronics Engineering • Mechanical Engineering • Thermal Engineering • Materials Science • Physics • Mathematics	• Device Physics • EDA • Lab Testing Equipment • IC Design Experience in CAD • Python, C#, C++, Assembly, Linux • CMOS Circuit Design, Layout, and Verification • Digital Simulation and Verification Flows	• Memory Design • AI/ML Frameworks (PyTorch, TensorFlow) & Models • Embedded Systems, Microcontrollers, and FPGA–Based Designs	• Atlanta, GA • Boise, ID • San Jose, CA • Richardson, TX
NAND/ ASIC Design Engineering	• IC Design Engineer • Verification/Validation Engineer	• BS • MS • PhD	• Computer Engineering • Electrical Engineering	• Computer Architecture • VLSI Design	• Digital Logic Design • Verilog/VHDL/ SystemVerilog	• Boise, ID • San Jose, CA • Longmont, CO • Minneapolis, MN
Manufacturing/ Operations	• Manufacturing Engineer • Process Engineer • Process Integration Engineer • Equipment Engineer • EHS Engineer • Facilities Engineer • Industrial Engineer	• BS • MS	• Chemistry/Chemical Engineering • Electrical Engineering • Environmental Engineering • Industrial Engineering • Mechanical Engineering • Microelectronics Engineering • Material Science • Supply Chain Management • Physics • Other STEM Degrees	• Statistics • SPC • Electromechanical Coursework *(for equipment engineer role)*	• Programming • Semi Process Lab • Semiconductor Materials • Semiconductor Physics	• Boise, ID • Manassas, VA • Clay, NY *(coming soon)*
Information Technology	• Software Engineer • Systems Manager • Database Administrator • Business Analyst • Project Manager • Software Developer	• BS • MS	• Computer engineering • Computer science • Computer information systems • Management information systems	• C++/C#, .Net, Python/ Angular • Linux, Unix, Windows • Cloud Platforms (AWS, Azure, GCP)	• SQL, NoSQL • SAP/ABAP, SSIS, SSRS • AI/ML, GenAI, Prompt Engineering • DevOps, CI/CD, Infrastructure as Code	• Boise, ID

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